

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

 Print FormatYour search matched **6** of **945031** documents.A maximum of **6** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.

You may refine your search by editing the current search expression or entering a new one the text box.

Then click **Search Again**.**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Adapting constant multipliers in a neural network implementation***James-Roxby, P.; Blodget, B.A.;*

Field-Programmable Custom Computing Machines, 2000 IEEE

Symposium on , 17-19 April 2000

Page(s): 335 -336

[\[Abstract\]](#) [\[PDF Full-Text \(180 KB\)\]](#) **IEEE CNF****2 A hardware-oriented algorithm for floating-point function generation***O'Grady, E.P.; Young, B.-K.;*

Computers, IEEE Transactions on , Volume: 40 Issue: 2 , Feb. 1991

Page(s): 237 -241

[\[Abstract\]](#) [\[PDF Full-Text \(524 KB\)\]](#) **IEEE JNL****3 A minimum time seek controller for a disk drive***Patten, W.N.; Wu, H.C.; White, L.;*

Magnetics, IEEE Transactions on , Volume: 31 Issue: 3 , May 1995

Page(s): 2380 -2387

[\[Abstract\]](#) [\[PDF Full-Text \(508 KB\)\]](#) **IEEE JNL****4 Bus architecture of a system on a chip with user-configurable system logic***Winegarden, S.;*

Solid-State Circuits, IEEE Journal of , Volume: 35 Issue: 3 , March

2000

Page(s): 425 -433

[\[Abstract\]](#) [\[PDF Full-Text \(280 KB\)\]](#) **IEEE JNL**

5 A reconfigurable content addressable memory*McAuley, A.J.; Cotton, C.J.;*Custom Integrated Circuits Conference, 1990., Proceedings of the
IEEE 1990 , 13-16 May 1990

Page(s): 24.1/1 -24.1/4

[\[Abstract\]](#) [\[PDF Full-Text \(420 KB\)\]](#) **IEEE CNF**

**6 A parallel architecture for multilevel decision feedback
equalization***Kenney, J.G.; Melas, C.M.;*

Magnetics, IEEE Transactions on , Volume: 34 Issue: 2 , March 1998

Page(s): 588 -595

[\[Abstract\]](#) [\[PDF Full-Text \(196 KB\)\]](#) **IEEE JNL**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

 Print FormatYour search matched **3** of **945031** documents.A maximum of **3** results are displayed, **25** to a page, sorted by **Relevance** in **descending** order.

You may refine your search by editing the current search expression or entering a new one in the text box.

Then click **Search Again**.**Results:**Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD****1 Adapting constant multipliers in a neural network implementation***James-Roxby, P.; Blodget, B.A.;*

Field-Programmable Custom Computing Machines, 2000 IEEE

Symposium on , 17-19 April 2000

Page(s): 335 -336

[\[Abstract\]](#) [\[PDF Full-Text \(180 KB\)\]](#) **IEEE CNF****2 A hardware-oriented algorithm for floating-point function generation***O'Grady, E.P.; Young, B.-K.;*

Computers, IEEE Transactions on , Volume: 40 Issue: 2 , Feb. 1991

Page(s): 237 -241

[\[Abstract\]](#) [\[PDF Full-Text \(524 KB\)\]](#) **IEEE JNL****3 A reconfigurable content addressable memory***McAuley, A.J.; Cotton, C.J.;*

Custom Integrated Circuits Conference, 1990., Proceedings of the

IEEE 1990 , 13-16 May 1990

Page(s): 24.1/1 -24.1/4

[\[Abstract\]](#) [\[PDF Full-Text \(420 KB\)\]](#) **IEEE CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

1) Enter a single keyword, phrase, or Boolean expression.
Example: acoustic imaging (means the phrase acoustic imaging plus any stem variations)

2) Limit your search by using search operators and field codes, if desired.

Example: optical (fiber fibre) ti

3) Limit the results by selecting Search Options.

4) Click Search. See [Search Examples](#)

((lookup <or> (look-up) <or> (look
<near/1> up)) <near/1> table) <and>
(read <and> write <and> memory)

Note: This function returns plural and suffixed forms of the keyword(s).

Search operators: [More](#)

Field codes: au (author), ti (title), ab (abstract), jn (publication name), de (index term) [More](#)

Search Options:**Select publication types:**

- ☒ IEEE Journals
- ☒ IEE Journals
- ☒ IEEE Conference proceedings
- ☒ IEE Conference proceedings
- ☒ IEEE Standards

Select years to search:

From year: to

Organize search results by:

Sort by:

In: order

List Results per page



Subscribe Register Login
(Full Service) (Limited Service, Free)

Search: ☐ The Guide ☒ The ACM Digital Library

lookup table read write memory

THE ACM DIGITAL LIBRARY

Feedback

Terms used lookup table read write memory

Sort results
by

relevance

Save results to a Binder

Try

Search Tips

Try

☐ Open results in a new window

Display results

expanded form

Results 1 - 20 of 200

Result page: **1** 2 3 4 5 6 7 8 9 1

Best 200 shown

1 Mondrian memory protection

Emmett Witchel, Josh Cates, Krste Asanovi?

October 2002 Tenth international conference on architectural support for programmir
Proceedings of the 10th international conference on architectural supp
operating systems (ASPLOS-X), Volume 37 , 30 , 36 Issue 10 , 5 , 5

Full text available: pdf(1.53 MB)

Additional Information: full citation, abstract, rel

Mondrian memory protection (MMP) is a fine-grained protection scheme that allow
share memory and export protected services. In contrast to earlier page-based s
control at the granularity of individual words. We use a compressed permissions l
employ two levels of permissions caching to reduce run-time overheads. The prot
less than 9% overhead to ...

2 Cache Memories

Alan Jay Smith

September 1982 ACM Computing Surveys (CSUR), Volume 14 Issue 3

Full text available: pdf(4.61 MB) Additional Information: full citation, references, citings, index terms

3 External memory algorithms and data structures: dealing with massive data

Jeffrey Scott Vitter

June 2001

ACM Computing Surveys (CSUR), Volume 33 Issue 2

Full text available:  pdf(828.46 KB)

Additional Information: full citation, abstract, references, citi

Data sets in large applications are often too massive to fit completely inside the c input/output communication (or I/O) between fast internal memory and slower e: major performance bottleneck. In this article we survey the state of the art in the (or EM) algorithms and data structures, where the goal is to exploit locality in orc varie ...

Keywords: B-tree, I/O, batched, block, disk, dynamic, extendible hashing, extern multidimensional access methods, multilevel memory, online, out-of-core, secon

4 Fine-grain access control for distributed shared memory

Ioannis Schoinas, Babak Falsafi, Alvin R. Lebeck, Steven K. Reinhardt, James R. La

November 1994 Proceedings of the sixth international conference on Architectural sup operating systems, Volume 29 , 28 Issue 11 , 5

Full text available:  pdf(1.20 MB)

Additional Information: full citation, abstract, references, citi

This paper discusses implementations of fine-grain memory access control, which cache-block-sized memory regions. Fine-grain access control forms the basis of e This paper focuses on low-cost implementations that require little or no additiona efficient implementation of shared memory on a wide range of parallel systems, t with a portability ...

5 Distributed file systems: concepts and examples

Eliezer Levy, Abraham Silberschatz

December 1990

ACM Computing Surveys (CSUR), Volume 22 Issue 4

Full text available:  pdf(5.33 MB)

Additional Information: full citation, abstract, references, citings

The purpose of a distributed file system (DFS) is to allow users of physically distr storage resources by using a common file system. A typical configuration for a DI mainframes connected by a local area network (LAN). A DFS is implemented as p connected computers. This paper establishes a viewpoint that emphasizes the dis both data and con ...

6 Efficient (stack) algorithms for analysis of write-back and sector memories

James G. Thompson, Alan Jay Smith

January 1989


ACM Transactions on Computer Systems (TOCS), Volume 7 Issu

Full text available:  pdf(2.93 MB)

Additional Information: full citation, abstract, references, citings

For the class of replacement algorithms known as stack algorithms, existing anal memory miss ratios for all memory sizes simultaneously in one pass over a memi of computations possible by this methodology in two ways. First, we show how to write-back caches. The key observation here is that a given block is clean for all i

- 7 The duality of memory and communication in the implementation of a multiprocessor
M. Young, A. Tevanian, R. Rashid, D. Golub, J. Eppinger
November 1987 ACM SIGOPS Operating Systems Review , Proceedings of the eleventh annual ACM symposium on operating systems principles, Volume 21 Issue 5

Full text available:  pdf(1.26 MB)

Additional Information: full citation, abstract, references, citations

Mach is a multiprocessor operating system being implemented at Carnegie-Mellon University. The Mach design is the use of memory objects which can be managed either by the kernel or by the user through a message interface. This feature allows applications such as transaction management, secondary storage management and page replacement. This paper explains the implementation of Mach and its ...

- 8 Session S4.1: power in memory and network processors: Embedded cache buffer support for power and performance flexibility
Afzal Malik, Bill Moyer, Roger Zhou
October 2002 Proceedings of the international conference on Compilers, architecture, and networking


Full text available:  pdf(122.19 KB)

Additional Information: full citation, abstract, references

Next generation portable devices are placing stringent requirements on overall system performance. Real-time recognition, streaming video and high speed wireless internet access are just some of the requirements for these handheld electronic gadgets. The M2CORE M341-S processor has been designed to support sensitive portable products as well as for high end embedded control applications over the M2CORE M2 and M310 families ...

Keywords: cache control, cache management, copyback, programmable, push buffer

- 9 Efficient strategies for software-only protocols in shared-memory multiprocessors
Håkan Grahn, Per Stenström
May 1995 ACM SIGARCH Computer Architecture News , Proceedings of the 22nd annual ACM symposium on Computer architecture, Volume 23 Issue 2

Full text available:  pdf(1.31 MB)

Additional Information: full citation, abstract, references, citations

The cost, complexity, and inflexibility of hardware-based directory protocols motivate the study of the implications of protocols that emulate directory management using software handling processors. An important performance limitation of such software-only protocols is that directory management ends up on the critical memory access path for read misses. This paper describes a support for efficient data transfers ...

10 Separating data and control transfer in distributed operating systems

Chandramohan A. Thekkath, Henry M. Levy, Edward D. Lazowska

November 1994 Proceedings of the sixth international conference on Architectural support for distributed operating systems, Volume 29 , 28 Issue 11 , 5

Full text available:  pdf(1.42 MB)

Additional Information: full citation, abstract, references, citations

Advances in processor architecture and technology have resulted in workstations and local-area networks such as ATM promise a ten- to hundred-fold increase in throughput, scalability, and greatly increased reliability, when compared to current LANs. Such network and processor technologies will permit tighter coupling of distributed systems.

11 The integration of virtual memory management and interprocess communication

Robert Fitzgerald, Richard F. Rashid

May 1986 ACM Transactions on Computer Systems (TOCS), Volume 4 Issue 2

Full text available:  pdf(2.45 MB)

Additional Information: full citation, abstract, references, citations

The integration of virtual memory management and interprocess communication kernel is examined. The design and implementation of the Accent memory management performance, both on a series of message-oriented benchmarks and in normal operation.

12 Efficient PRAM simulation on a distributed memory machine

Richard M. Karp, Michael Luby, Friedhelm Meyer auf der Heide

July 1992 Proceedings of the twenty-fourth annual ACM symposium on Theory of Computing

Full text available:  pdf(815.15 KB)

Additional Information: full citation, abstract, references, citations

We present a randomized simulation of a $n \log \log(n) \log(n)$ -processor shared memory PRAM with expected delay $O(\log \log(n))$ per step of simulation. The time bound for the delay is with high probability. The algorithm is based on hashing and uses a novel simulation scheme. A simpler scheme based on hashing and have much larger expected delay: $\Theta(n \log \log(n))$.

13 Analytic evaluation of shared-memory systems with ILP processors

Daniel J. Sorin, Vijay S. Pai, Sarita V. Adve, Mary K. Vernon, David A. Wood

April 1998 ACM SIGARCH Computer Architecture News , Proceedings of the 25th annual ACM symposium on Computer architecture, Volume 26 Issue 3

Full text available:  pdf(1.45 MB)

Additional Information: full citation, abstract, references, citations

This paper develops and validates an analytical model for evaluating various types of shared-memory systems with processors that aggressively exploit instruction-level parallelism. The analytical model is many orders of magnitude faster to solve, yielding highly accurate results in seconds. The model input parameters characterize the ability of an application to exploit parallelism as well as the interaction between the application and the hardware.

14 Improving the efficiency of UNIX buffer caches

A. Braunstein, M. Riley, J. Wilkes

November 1989 ACM SIGOPS Operating Systems Review , Proceedings of the twelfth principles, Volume 23 Issue 5

Full text available:  pdf(1.46 MB)

Additional Information: full citation, abstract, references

This paper reports on the effects of using hardware virtual memory assists in main controlled experimental environment was constructed from two systems whose o (XMF) used the virtual memory hardware to assist file buffer cache search and re performance characterizations was used to study the effects of varying the buffer MB); I/O transfer sizes (from ...

15 Memory-efficient state lookups with fast updates

Sandeep Sikka, George Varghese

August 2000 ACM SIGCOMM Computer Communication Review , Proceedings of the c Architectures, and Protocols for Computer Communication, Volume 30

Full text available:  pdf(384.82 KB)

Additional Information: full citation, abstract, reference

Routers must do a best matching prefix lookup for every packet; solutions for Gig link speeds higher, we seek a scalable solution whose speed scales with memory databases. In this paper we show that providing such a solution requires careful ; pipelining. This is because fast lookups require on-chip or off-chip SRAM which is

16 Distributed operating systems

Andrew S. Tanenbaum, Robbert Van Renesse

December 1985 ACM Computing Surveys (CSUR), Volume 17 Issue 4

Full text available:  pdf(5.49 MB)

Additional Information: full citation, abstract, references, citations,

Distributed operating systems have many aspects in common with centralized on This paper is intended as an introduction to distributed operating systems, and es about them. After a discussion of what constitutes a distributed operating system computer network, various key design issues are discussed. Then several exampl examined in some detail ...

17 An efficient I/O interface for optical disks

Jeffrey S. Vitter

June 1985 ACM Transactions on Database Systems (TODS), Volume 10 Issue 1

Full text available:  pdf(2.55 MB)

Additional Information: full citation, abstract, references, citations,


We introduce the notion of an I/O interface for optical digital (write-once) disks, a research. The purpose of an I/O interface is to allow existing operating systems a magnetic disks to use optical disks instead, with minimal change. We define what disk-efficient. We demonstrate a practical disk- efficient I/O interface and show tl optimum, up to a ...

18 Query evaluation techniques for large databases

Goetz Graefe

June 1993

ACM Computing Surveys (CSUR), Volume 25 Issue 2

Full text available:  pdf(9.37 MB)

Additional Information: full citation, abstract, references, citings,

Database management systems will continue to manage large data volumes. Thus manipulating large sets and sequences will be required to provide acceptable performance and extensible database systems will not solve this problem. On the contrary, the problem: In order to manipulate large sets of complex objects as efficiently as to simple records, query-processing ...

Keywords: complex query evaluation plans, dynamic query evaluation plans, extended object-oriented database systems, operator model of parallelization, parallel algorithm set-matching algorithms, sort-hash duality

19 Architectural support for scalable speculative parallelization in shared-memory

Marcelo Cintra, José F. Martínez, Josep Torrellas

May 2000 ACM SIGARCH Computer Architecture News , Proceedings of the 27th annual architecture, Volume 28 Issue 2

Full text available:  pdf(253.29 KB)


Additional Information: full citation, abstract, references, citings,

Speculative parallelization aggressively executes in parallel codes that cannot be proposed by hardware schemes have mostly focused on single-chip multiprocessing necessarily limited by their small size. Very few schemes have attempted this technique in shared-memory systems. In this paper, we present and evaluate a new hardware parallelization. This design ...

20 Machine-independent virtual memory management for paged uniprocessor architectures

Richard Rashid, Avadis Tevanian, Michael Young, David Golub, Robert Baron

October 1987 Proceedings of the second international conference on Architectural support for operating systems, Volume 15 , 22 , 21 Issue 5 , 10 , 4

Full text available:  pdf(1.21 MB)

Additional Information: full citation, citings, index

Results 1 - 20 of 200

Result page: **1** 2 3 4 5 6 7

The ACM Portal is published by the Association for Computing Machinery. C

[Terms of Usage](#) [Privacy Policy](#) [Code of Ethics](#) [Contact Us](#)Useful downloads:  Adobe Acrobat  QuickTime  Windows Media

[Home](#)[Editorial Guidelines](#) | [Help](#)

Contains A Lookup Table memory read write

Search: ☐ Web ☐ Pictures ☐ News ☐ Products**SUPERPAGES.COM**
BETTER PAGES FOR BETTER DECISIONS

SPONSORED CONTENT

Search SuperPages.com for:

Business name: or business category:
city: state:

You may also be interested in these other search options.

Detailed: Search by street, zip code and more Distance: Search near an address

Sponsored Web Results**Computer Memory Upgrade**Guaranteed Compatibility, Easy to Use Website, Free Shipping, No Tax
From: www.4AllMemory.com**Computer Memory 256MB \$33**Wholesale Computer **Memory** 40% off Lifetime Warranty - Free Shipping!
From: www.oempcworld.com**Lookup Anyone**Find Any Unlisted Number & Address. Updated Regularly, Highly Accurate.
From: www.intelius.com**Free Writing Software**Plus Free online Writing Classes, Downloads, & Tips at Storymind
From: storymind.com**Web Results****AFS Frequently Asked Questions**This posting **contains** answers to ... 3.07 How does AFS maintain consistency on **read-write** files? ... when a cold **read** is necessary. (Up to **Table** of....
From: www.angelfire.com/hi/plutonic/afs-faq.html**Dan Boris: Understanding MAME Drivers 1: Data Structures**...make up the game's palette and the third is a pointer to a color **lookup table**. ... The pointers are **Memory Read, Memory Write, IO Read, IO Write**.
From: atarihq.com/danb/files/mamedrv1.txt**CmpSci 535 Lecture 9**...output data directly from **memory**. Because the mameory **contains** stale ... **table** entry and if there is a mtch and the type of access (**read** vs.
From: www.cs.umass.edu/~weems/CmpSci535/535lecture9.html**JCN: A netbooting Linux system not dependent on a read/write NFS**Linux system not dependent on a **read/write** NFS ... small amounts of **memory**, /local **contains** a ... simply compares PCI ids to a **lookup table** and spits...
From: www-jcsu.jesus.cam.ac.uk/jcn/documentation/netlinux/netlinux.html

ChangeLog-2.5.45

...dummy **read** check into the **read** ... c In 2.5.44 it **contains** only two ... [IPV4]: Rework key route **lookup** interface ... [SCSI] fix **memory** etc. leak....

From:www.kernel.org/pub/linux/kernel/v2.5/ChangeLog-2.5.45

FAQ: Network Intrusion Detection Systems

...take apart the system and remove the disk drive (and **read/write** it on ... must first **read** the file into **memory** ... The intruder will do a 'whois'

From:www.robertgraham.com/pubs/network-intrusion-detection.html

Untitled

...that stored in database without using a **lookup table**. ... allows you to **read** and **write** virtually any ... TMemoryTable implements BDE in-**memory**...

From:delphi.stts.edu/ftp/d10free/rxlib250.txt

AidAim Software

It **contains** two ... indexes, shareable in **memory** ... It supports calculated and **lookup** fields. ... - **Read** and **write** any portion of data at any file.....

From:delphi.icm.edu.pl/authors/a0003682.htm

ReiserFS

If you **write** 100 byte ... References Also **Read** ... due to the **memory** bandwidth ... directory at a time **lookup** accomplishes a ... which formatted node...

From:www.namesys.com/

BYTE.com

...frequently updated, they reside in an area of **memory** with **read/write** access. ... patching the trap **table**--to function. While the **lookup-table**...

From:www.byte.com/art/9404/sec6/art1.htm

[More Web Results»](#)

Search for *Contains A Lookup Table memory read write* on other sites:

Search Local Yellow Page Listings for **Contains A Lookup Table memory read write** sponsored by SMARTpages.com

Contains A Lookup Table memory read write



> **Download** the Ask Jeeves Toolbar

Search: **Web** **Pictures** **News** **Products**


[Advanced Search](#) [Preferences](#) [Language Tools](#) [Search Tips](#)

[Web](#) · [Images](#) · [Groups](#) · [Directory](#) · [News](#)

Searched the web for "adrian j. isles" memory model. Results 1 - 10 of about 35. Search took 0.28 seconds.

Verification Using Datapath Abstraction

... Using Uninterpreted Functions and Infinite **Memory**," Proc. ... Sequential Circuit Verification

Using Symbolic **Model** Checking," ACM ... mail to **Adrian J. Isles** : (aji@eecs ...
www.eecs.berkeley.edu/IPRO/Summary/ 98abstracts/aji.1.html - 6k - [Cached](#) -

[Similar pages](#)

Sponsored Links

Computer Memory Upgrade

Guaranteed Compatibility, Easy to Use Website, Free Shipping, No Tax
www.4AllMemory.com
Interest:

Formal Verification Using a Network of Workstations

Adrian J. Isles, Amit Narayan, and Jawahar Jain 1 ... For a given **memory** limit, the POBDD based reachability ... the use of NOWs for performing CTL **model** checking and ...

www.eecs.berkeley.edu/IPRO/Summary/ 98abstracts/aji.3.html - 9k - [Cached](#) - [Similar pages](#)

[[More results from www.eecs.berkeley.edu](#)]

Verification IP - **Memory**

High-quality models from Denali support all device types & vendors
www.ememory.com
Interest:

[See your message here...](#)

CAV 1998

... Song, Francisco Corella, Otmane Aït Mohamed: **Model** Checking for ... 244-255; **Adrian J. Isles**, Ramin Hojati ... Modeled with Uninterpreted Functions and Infinite **Memory**. ...

www.informatik.uni-trier.de/~ley/db/conf/cav/cav98.html - 17k - [Cached](#) - [Similar pages](#)

DBLP: Ramin Hojati

... K. Brayton: Structural Symmetry and **Model** Checking. ... 13, **Adrian J. Isles**, Ramin Hojati, Robert K ... Systems Modeled with Uninterpreted Functions and Infinite **Memory**. ...

www.informatik.uni-trier.de/~ley/db/indices/a-tree/h/Hojati:Ramin.html - 9k - [Cached](#) - [Similar pages](#)

[[More results from www.informatik.uni-trier.de](#)]

ICCAD'98 Table of Contents

... Interpretation Pei-Hsin Ho, **Adrian J. Isles**, Timothy Y ... a Submicron Transistor-Level Delay **Model** Pasquale Cocchini ... CA 10C.1 Removal of **Memory** Access Bottlenecks ...

cadlab.cs.ucla.edu/~kohcc/sigdacdrom/iccad98/papers/ 1998/iccad98/htmfiles/sun_sgi/frames/iccadtoc.htm - 45k - [Cached](#) - [Similar pages](#)

[PDF] TABLE OF CONTENTS

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... 2 Adaptive Variable Reordering for Symbolic **Model** Checking Gila ... Software Co-Synthesis with **Memory** Hierarchies Yanbing ... Pei-Hsin Ho, **Adrian J. Isles**, Timothy Kam ...

www.sigda.org/Archives/ProceedingArchives/ Iccad/Last20/Papers/1998/ICCAD98_TOC.PDF - [Similar pages](#)

[PDF] Reachability Analysis Using Partitioned-ROBDDs

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Reachability Analysis Using Partitioned-ROBDDs Amit Narayan **Adrian J. Isles** Jawahar Jain 1 ... the use of partitioned-ROBDDs to reduce the **memory** explosion problem ...

www.sigda.org/Archives/ProceedingArchives/Iccad/ Iccad97/papers/1997/iccad97/pdffiles/07a_1.pdf - [Similar pages](#)

[[More results from www.sigda.org](#)]

Abstracts for Robert K. Brayton

... For verification: (1) "fair-CTL" **model** checking, which uses ... **Adrian J. Isles** and Amit

Narayan (Professors Robert K ... are too big to fit in computer **memory** or take ...
buffy.eecs.berkeley.edu/IRO/Summary/ 97abstracts/abstracts.RKB.html - 51k - [Cached](#) - [Similar pages](#)

[PDF] [Formal Verification of Pipeline Control Using Controlled Token ...](#)

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... For the capacity issue, existing **model** checkers cannot consistently verify designs
with more ... pho@synopsys.com **Adrian J. Isles** Department of EECS University of ...
www.geocities.com/SiliconValley/Lab/8478/iccad98.pdf - [Similar pages](#)

[PS] [www.informatik.uni-kiel.de/~umi/Narayan1.ps.gz](#)

File Format: Adobe PostScript - [View as Text](#)

... ROBDDs Amit Narayan **Adrian J. Isles** Jawahar Jain1 ... For a given **memory** limit, the partitioned-ROBDD ...
Symbolic

Model Checking with Partitioned Transition Relations. ...

[Similar pages](#)

Google ►

Result Page: 1 2 [Next](#)

"adrian j. isles" memory model

Google Search

[Search within results](#)

Dissatisfied with your search results? [Help us improve.](#)

[Google Home](#) - [Advertise with Us](#) - [Business Solutions](#) - [Services & Tools](#) - [Jobs, Press, & Help](#)

©2003 Google


[Advanced Search](#) [Preferences](#) [Language Tools](#) [Search Tips](#)

[Web](#) - [Images](#) - [Groups](#) - [Directory](#) - [News](#)

Searched the web for "**adrian isles**" (**lookup OR (look up)**). Results **1 - 7** of about **8**. Search took **0.36** seconds.
Try [Google Answers](#) to get help from expert researchers.

Devel-L: Future Black Faculty Database

... Take the time and sign **up** today. ... To find out more, **look** at these URL's: FBF Database:
http ... Sutton, Brian Dennis, Jeff Forbes, Ray Gilstrap, **Adrian Isles** and the ...
library.wustl.edu/~listmgr/devel-l/Dec1995/0118.html - 6k - [Cached](#) - [Similar pages](#)

Sponsored Links

Lookup Anyone

Find Any Unlisted Number & Address.
Updated Regularly, Highly Accurate.
www.intelius.com
Interest:

CS 252 - Graduate Computer Architecture

... Please take this opportunity to pick **up** any other remaining items (homeworks ... Fly
Cache Compression and Decompression , Eric Kusse and **Adrian Isles** Design and ...
www.cs.berkeley.edu/~pattrsn/252F96/ - 27k - Jun 26, 2003 - [Cached](#) - [Similar pages](#)

[See your message here...](#)

[PDF]CAD for Synthesis and Verification of Interaction FSMs Prof. ...

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Using Datapath Abstraction", PhD Thesis, **Adrian Isles**, May 2000 The ... We then **look**
at MV-network manipulation operations ... work has been written **up** and submitted ...
www.ucop.edu/research/micro/99_00/99_012.pdf - [Similar pages](#)

Untitled

... of Boolean Functions for Table **Look Up** Architectures, (R ... Exploiting Power-**up** Delay
for Sequential Optimization ... **Adrian Isles**, Ramin Hojati and Robert K. Brayton ...
www-cad.eecs.berkeley.edu/HomePages/brayton/pubs296/pubs296.html - 79k - [Cached](#) - [Similar pages](#)

[ps]www-cad.eecs.berkeley.edu/~rajeev/publications/psdir/phdThesis.ps

File Format: Adobe PostScript - [View as Text](#)

... **Adrian Isles** has been a good cubicle mate to me. ... South America (I am not making this
all **up**, I actually ... Another way to **look** at the affect of increasing design ...
[Similar pages](#)

[PDF]01 Spring News

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... in how we can work together." Asked about MIR's contribution to technology, Dr. Mueller
said, "It keeps peoples' interest **up**, and it's been ... I **look** forward to ...
www.eecs.berkeley.edu/IPRO/01.Spring.News.pdf - [Similar pages](#)

[PDF]Reinforcement Learning for Autonomous Vehicles

File Format: PDF/Adobe Acrobat - [View as HTML](#)

... Lexi Hazam, Kemba Extavour, John Davis, **Adrian Isles**, Robert Stanard ... Do you turn
your head and **look** back before ... module itself can be split **up** further where it ...
www.cs.duke.edu/~forbes/thesis/thesis.pdf - [Similar pages](#)

Dissatisfied with your search results? [Help us improve.](#)

[Google Home](#) - [Advertise with Us](#) - [Business Solutions](#) - [Services & Tools](#) - [Jobs, Press, & Help](#)

©2003 Google

[Advanced Search](#) [Preferences](#) [Language Tools](#) [Search Tips](#)[Web](#) · [Images](#) · [Groups](#) · [Directory](#) · [News](#)

Searched the web for "**adrian j. isles**" "**memory model**". Results **1 - 1** of about **2**. Search took **0.24** seconds.
Try [Google Answers](#) to get help from expert researchers.

Computer-Aided Design for 1998.

... [www-cad.eecs.berkeley.edu](#)) or Send mail to **Adrian J. Isles** : (aji@eecs ...
built-in,
low-level concurrency in the form of threads, and its explicit **memory model**. ...
[www.eecs.berkeley.edu/IPRO/Summary/98abstracts/chapter1.html](#) - 101k - Jun
26, 2003 - [Cached](#) - [Similar pages](#)

Sponsored Links

Computer Memory Upgrade

Guaranteed Compatibility, Easy to
Use Website, Free Shipping, No Tax
[www.4AllMemory.com](#)
Interest:

Verification IP - Memory

High-quality models from Denali
support all device types & vendors
[www.ememory.com](#)
Interest:

[See your message here...](#)

[Search within results](#)

Dissatisfied with your search results? [Help us improve.](#)

[Google Home](#) - [Advertise with Us](#) - [Business Solutions](#) - [Services & Tools](#) - [Jobs, Press, & Help](#)

©2003 Google


[Advanced Search](#) [Preferences](#) [Language Tools](#) [Search Tips](#)

[Web](#) - [Images](#) - [Groups](#) - [Directory](#) - [News](#)

Searched the web for "**adrian j. isles**" (**lookup OR ("look up")**). Results **1 - 4** of about **9**. Search took **0.25** seconds.
Try [Google Answers](#) to get help from expert researchers.

DBLP: Robert K. Brayton

... 89, Ramin Hojati, **Adrian J. Isles**, Desmond Kirkpatrick, Robert K. Brayton ...

L. Sangiovanni-Vincentelli:

Sequential Synthesis for Table **Look Up** Programmable Gate ...

www.informatik.uni-trier.de/~ley/db/indices/a-tree/b/Brayton:Robert_K=.html - 64k

- [Cached](#) - [Similar pages](#)

Sponsored Links

Lookup Anyone

Find Any Unlisted Number & Address.

Updated Regularly, Highly Accurate.

www.intelius.com

Interest:

[See your message here...](#)

DBLP: Alberto L. Sangiovanni-Vincentelli

... 125, EE, Amit Narayan, **Adrian J. Isles**, Jawahar Jain, Robert K ... Alberto L. Sangiovanni-Vincentelli:

Sequential Synthesis for Table **Look Up** Programmable Gate Arrays ...

www.informatik.uni-trier.de/~ley/db/indices/a-tree/s/Sangiovanni=Vincentelli:Alberto_L=.html - 87k -

[Cached](#) - [Similar pages](#)

Abstracts for Robert K. Brayton

... www-cad.eecs.berkeley.edu) or Send mail to **Adrian J. Isles** : ([aji@eecs ... 1](mailto:aji@eecs.berkeley.edu)] introduced a new way to express functional permissibilities for **look-up** table based ...

buffy.eecs.berkeley.edu/IRO/Summary/98abstracts/abstracts.RKB.html - 34k - [Cached](#) - [Similar pages](#)

ICCAD98'98 Abstracts

... gamma distribution functions, the delays could be obtained via table **lookup** using

a ... Nets and Abstract Interpretation Pei-Hsin Ho, **Adrian J. Isles**, Timothy Y ...

cadlab.cs.ucla.edu/~kohcc/sigdacdrom/iccad98/papers/1998/iccad98/htmfiles/sun_sgi/frames/iccadabs.htm -

101k - [Cached](#) - [Similar pages](#)

In order to show you the most relevant results, we have omitted some entries very similar to the 4 already displayed.

If you like, you can repeat the search with the omitted results included.

[Search within results](#)

Dissatisfied with your search results? [Help us improve.](#)

[Google Home](#) - [Advertise with Us](#) - [Business Solutions](#) - [Services & Tools](#) - [Jobs, Press, & Help](#)

©2003 Google



Membership Publications/Services Standards Conferences Careers/Jobs

IEEE Xplore®
RELEASE 1.4

Welcome
United States Patent and Trademark Office

Help FAQ Terms IEEE Quick Links

Peer Review [» Author Search](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out
- Tables of Contents
- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards
- Search
- ☐ By Author
- ☐ Basic
- ☐ Advanced
- Member Services
- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

Quick Find an Author:

Enter a last name to quickly locate articles by that author.

Go

Note: You may enter a partial name if your are unsure of the spelling.

OR

Select a letter to browse the author list

[A](#) [B](#) [C](#) [D](#) [E](#) [F](#) [G](#) [H](#) [I](#) [J](#) [K](#) [L](#) [M](#) [N](#) [O](#) [P](#) [Q](#) [R](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#) | [ALL](#)

[Isles A. J.](#)

[A](#) [B](#) [C](#) [D](#) [E](#) [F](#) [G](#) [H](#) [I](#) [J](#) [K](#) [L](#) [M](#) [N](#) [O](#) [P](#) [Q](#) [R](#) [S](#) [T](#) [U](#) [V](#) [W](#) [X](#) [Y](#) [Z](#) | [ALL](#)

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)

[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#) [No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Welcome to IEEE Xplore®

- ☐ Home
- ☐ What Can I Access?
- ☐ Log-out

Tables of Contents

- ☐ Journals & Magazines
- ☐ Conference Proceedings
- ☐ Standards

Search

- ☐ By Author
- ☐ Basic
- ☐ Advanced

Member Services

- ☐ Join IEEE
- ☐ Establish IEEE Web Account
- ☐ Access the IEEE Member Digital Library

 [Print Format](#)

Your search matched **2** of **945031** documents.
Results are shown **15** to a page, sorted by **publication year** in **descending** order.

Results:

Journal or Magazine = **JNL** Conference = **CNF** Standard = **STD**

1 Formal verification of pipeline control using controlled token nets and abstract interpretation

Pei-Hsin Ho; Isles, A.J.; Kam, T.;

Computer-Aided Design, 1998. ICCAD 98. Digest of Technical Papers. 1998 IEEE/ACM International Conference on , 8-12 Nov. 1998

Page(s): 529 -536

[\[Abstract\]](#) [\[PDF Full-Text \(872 KB\)\]](#) **IEEE CNF**

2 Reachability analysis using partitioned-ROBDDs

Narayan, A.; Isles, A.J.; Jain, J.; Brayton, R.K.;

Sangiovanni-Vincentelli, A.L.;

Computer-Aided Design, 1997. Digest of Technical Papers., 1997 IEEE/ACM International Conference on , 9-13 Nov. 1997

Page(s): 388 -393

[\[Abstract\]](#) [\[PDF Full-Text \(612 KB\)\]](#) **IEEE CNF**

[Home](#) | [Log-out](#) | [Journals](#) | [Conference Proceedings](#) | [Standards](#) | [Search by Author](#) | [Basic Search](#) | [Advanced Search](#)
[Join IEEE](#) | [Web Account](#) | [New this week](#) | [OPAC Linking Information](#) | [Your Feedback](#) | [Technical Support](#) | [Email Alerting](#)
[No Robots Please](#) | [Release Notes](#) | [IEEE Online Publications](#) | [Help](#) | [FAQ](#) | [Terms](#) | [Back to Top](#)

Copyright © 2003 IEEE — All rights reserved